April 1st, 4pm EST

The Trusted and Assured Microelectronics (T&AM) Program sponsors Multi-Project Wafer (MPW) runs to enable access to SOTA US commercial foundries \leq 22nm in support of Quantifiable Assurance goals and to aid in the developing DoD specific PDK's and IP.

The program is available to industry, Gov't labs and universities with projects that support DoD goals. Current technologies available are Global Foundries 12LP through DMEA, Trusted Access Program Office and Intel 22FFL through MOSIS.

The panel will provide an overview of the program with Q&A to follow.

Panel Participants:

Morgan Thoma – Panel Lead, OUSD R&E, Trusted and Assured Microelectronics Program Saverio Fazzari – DARPA, Microsystems Technology Office John Lloyd - DMEA, Trusted Access Program Office Devanand Shenoy – Information Sciences Institute, University of Southern California